AMENDMENTS TO THE SPECIFICATION

1- Please replace the two full paragraphs starting at line 3 of page 5 and ending at line 18 of page 5 by the following two paragraphs:

Analogously, for the present invention operating in a 1x mode, as shown in Figure 3B, the switches (transistors) MP1 and MP3 are closed. Additionally, transistors MN2, MN3, MN5, and MN6 are closed. The result is a negative battery voltage presented on the output capacitor C4. Capacitors C1, C2, and C3 are referred to as charging capacitors. Capacitor C1 is connected to a high and a low voltage rail. ~~are~~ In this embodiment capacitors C2 and C3 are connected through switches MP1, ~~MP2,~~ and MP3 to a high voltage rail. ~~The capacitors C1, C2, and C3 is~~ and also connected to a low voltage potential through switches ~~MN1~~MN2 and ~~MN6~~MN5, respectively. Similarly, the output capacitor C4 is connected between a low (negative) voltage potential ~~(typically the substrate)~~ and a high voltage rail.

The circuit topology during a charging state and a boosting state for the present invention is shown in Figure 4B. In the charging state, MP1, MP2, and MN5 are all closed. This results in the circuit shown in Figure 4B for the charging state. During the boosting state, the transistors MN1, MN3, MN4, and MN6 ~~and MP6~~ are all closed. The remaining transistors are open. The resulting circuit is shown in Figure 4B. As seen in Figure 4B, the resulting circuit provides an output voltage across the capacitor C4 of a 1.5 times the battery voltage VB. The analogous charging and boosting circuitry for the prior art positive charge pump is shown in Figure 4A.